More players exhibiting a common IP strategy for power & RF GaN technologies

KnowMade surveys the GaN electronics patent landscape in 2023.

n a new GaN electronics intellectual property (IP) report released by technology intelligence and IP strategy consulting company KnowMade, the patent landscapes for both the power GaN and RF GaN electronics sectors have been analyzed to describe the global IP competition across the whole supply chains and the local ecosystems emerging to support the industrialization of GaN technologies.

More players exhibiting a common IP strategy for power GaN and RF GaN technologies

In addition to power GaN and RF GaN patents, Know-Made's GaN Electronics IP report considers the impact of generic GaN electronics patents, applicable to both power and RF applications, on the global IP competition (Figure 1). For example, some companies such as United Microelectronics Corp (UMC) don't limit the application scope in most of their GaN electronics inventions, showing a common IP strategy for both RF and power markets. Also, well-established power GaN market players, such as Infineon Technologies and Innoscience, own a large number of generic GaN electronics patents that could be leveraged for RF applications in the next few years.

Power GaN patent landscape: a focus on national and regional ecosystems

Inventive activity used to be dominated by Japanese players (2001–2015) until Chinese players took over the IP leadership in 2016. As a result, Japanese and Chinese players have produced more than 70% of all power GaN inventions collectively (Figure 2). Such



Figure 1: Main companies in the GaN electronics patent landscape.

intensive patenting activities are eclipsing important IP trends occurring in other regions (USA, Europe, etc). KnowMade's GaN Electronics report unveils such IP trends, providing separate analyses of the regional ecosystems in the power GaN patent landscape.

The GaN Electronics Patent Landscape report points out that most of the historical Japanese IP players are focusing on monetizing their power GaN IP portfolios (Sharp, Furukawa Electric, NTT, Fujitsu). Few of them are still actively filing patent applications to consolidate their IP position, apart from Fujitsu and Panasonic, in different parts of the supply chain. However, new Japanese IP leaders such as ROHM and Sumitomo Chemical are now aiming to turn their IP leadership into market leadership.

Taiwanese players are rising in the power GaN IP landscape, with most of the main Taiwanese foundries ramping up their activities across the power GaN supply chain, following TSMC's lead. The Taiwanese IP activities highlight a reinforcement of the domestic supply chain for power GaN technology, with major players actively filing patents in the upstream supply chain (e.g. GlobalWafers) and accelerating their patent filings in the downstream supply chain (e.g. Delta Electronics). In addition, several newcomers have recently entered the power GaN IP landscape in Taiwan.

The US-China trade war adds a new dimension to power GaN IP competition, urging players to adapt their strategy with a view to securing the development of their power GaN activities internationally. In this context, several Chinese players such as Innoscience and Huawei are expanding their IP activities worldwide, looking to compete in the US and European markets. Most of the main US market players don't have complete IP coverage of the power GaN supply chain. Instead, US players leverage IP and manufacturing partnerships and/or existing IP and know-how developed for other power semiconductor technologies (silicon, silicon carbide). According to their own patenting activities, the main US market players are consolidating their own IP position in Asia and Europe, to support the development of their market activities outside the USA.

Infineon is the main vertically integrated innovator in the power GaN IP landscape, with a global IP strategy that aims to cover the main important regions for power electronics. Since 2015, Infineon has successfully leveraged multiple acquisitions (GaN Systems, International Rectifier) and IP partnerships (Panasonic) with a view to accelerating its strategy in the power GaN market. In Europe, major research organizations (CEA, imec, Fraunhofer) are driving the establishment of a domestic supply chain, leading to the establishment of new startup companies, and to the emergence of more vertically integrated innovators such as STMicroelectronics.

RF GaN patent landscape: a look into the IP strategies of key players across the supply chain

The GaN Electronics Patent Landscape report highlights different views from RF GaN market players about what innovation will be critical to protect in order to impact the future RF GaN supply chain (Figure 3). For example, several incumbent players in the RF GaN market seem to consider wafer and epiwafer IP as less critical, while others, such as Sumitomo Electric, Raytheon and Mitsubishi Electric, are still competing in



Figure 2: Main players in the power GaN patent landscape, split by country.



Figure 3: IP leadership of patent assignees in the upstream part (wafers, epiwafers, devices) and the downstream part (packaging, modules, circuits, applications) of the RF GaN supply chain.

this IP space. Most vertically integrated players identified in the report are still focusing on the IP related to RF GaN devices. In this regard, NXP stands out as it soon shifted its focus to the downstream supply chain. It was followed by most of the main RF GaN market players, who are now consolidating their IP position in packaging, modules, circuits and applications.

In recent years, Wolfspeed has taken over the IP leadership across the whole RF GaN supply chain, except for wafers and epiwafers (Figure 3). Just like Wolfspeed, MACOM has been actively filing patents across the RF GaN supply chain. Yet, unlike Wolfspeed, MACOM's IP activity did not translate into global IP leadership. With the acquisition of Wolfspeed's RF business, MACOM is expected to catch up with the competition in the global RF GaN IP landscape. Interestingly, this acquisition virtually positions MACOM as an indisputable IP leader for circuits and applications. Another stand-out player emerging in the RF GaN patent landscape is Mitsubishi Electric: it is currently the only vertically integrated innovator that is still competing across the whole RF GaN supply chain.

In contrast with the power GaN patent landscape, US players have established complete IP coverage of the RF GaN supply chain, and this IP ecosystem has been reinforced by many start-up companies actively filing patent applications in different part of the supply chain during the last decade (Akoustis, Akash Systems, Eridan, Finwave, etc). The analysis of their IP strategies shows that US companies are now expanding their patenting activities outside their national territory, especially in

Europe, China and Taiwan, to support their international ambitions in the growing RF GaN market.

In the wafer and epiwafer IP space, the competition is now taking multiple and different directions: GaN-on-Si (IQE, Shin Etsu), GaN-on-diamond (RFHIC, Akash Systems), GaN-on-engineered substrates (Soitec, Qromis, Shin Etsu), GaN-on-AIN (Fujitsu), in addition to the mainstream GaN-on-SiC platform (Sumitomo Electric, Sumitomo Chemical). Regarding the GaN-on-Si platform, the GaN Electronics IP report highlights a reduction in patent filings from most well-established IP players. In this context, Intel continues to lead the competition in the RF GaN-on-Si IP landscape, especially for RF GaN-on-Si devices. Aside from Intel, MACOM and TSMC are the main established IP players still actively filing patent applications for RF GaN-on-Si technology. Interestingly, the other Taiwanese foundries are following TSMC's lead. What's more, many power GaN IP players were seen to file RF GaN patent applications lately (Innoscience, Infineon, ST), indicating the development of RF GaN devices with a view to entering the RF telecom market with GaN-on-Si and/or other unconventional platforms such as engineered substrates (Qromis-VIS) or semi-insulating SiC on conductive SiC substrates (ROHM).

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