## UV-C LEDs with external quantum efficiency up to 8%

## Achieving improved performance from growth on 'face-to-face' annealed sputtered AIN templates on sapphire.

Researchers in Japan have used 'face-to-face' annealed (FFA) sputtered AIN (Sp-AIN) templates on sapphire to fabricate 263nm ultraviolet-C (UV-C) light-emitting diodes (LEDs) with state-of-the-art external quantum efficiency (EQE) [Kenjiro Uesugi et al, Appl. Phys. Express, v15, p055501, 2022].

The team from Mie University, Osaka University and University of Tokyo comments: "To the best of our knowledge, the developed UV-C LED has the highest EQE at a wavelength of approximately 265nm, even considering the state-of-the-art devices fabricated on sapphire substrates and free-standing AIN substrates."

UV-C LEDs are sought to replace bulky, fragile and hazardous mercury lamps used for sterilization and virus inactivation, needing wavelengths shorter than 300nm. Presently, UV-C LEDs have low EQEs of less than 10%. The reported LEDs achieved EQEs up to 8% when the devices were suitably packaged and encapsulated.

The team has previously used its FFA Sp-AlN growth technique to reduce threading dislocation densities (TDDs) by an order of magnitude to  $4x10^7$ /cm<sup>2</sup>.

The Sp-AIN was deposited on sapphire substrate with a 0.2° off-angle in the m-direction. Two samples were grown at 700 and 750°C, respectively.

The wafers were subject FFA at 1600–1700°C for 36 hours. The FFA arrangement involves placing two wafers with the two AIN surfaces in contact 'face-to-face', avoiding thermal decomposition of the AIN at such high temperature in nitrogen at atmospheric pressure.

The resulting FFA Sp-AIN layer was 600nm thick. This was followed by metal-organic vapor phase epitaxy (MOVPE) of 200nm AIN and 300nm unintentionally doped Al<sub>0.7</sub>Ga<sub>0.3</sub>N. Life being what it is, the reduced threading dislocation density of the underlying AIN leads to increased fractions of screw- and mixed-type dislocations that present as "excessively large" hillock structures on the wafer surface (Figure 1).



Figure 1. Nomarski microscopy images of 300nm-thick Al<sub>0.7</sub> Ga<sub>0.3</sub>N films grown on FFA Sp-AlN fabricated at sputter-deposition temperature of (a) 700°C, (b) 750°C. Inset: magnified image of typical hillock structures.

Contact	GaN:Mg	150nm
Graded p-type	Al <sub>0.95-0.40</sub> Ga <sub>0.05-0.60</sub> N:Mg	15nm
Electron-blocking	AIN	7nm
MQW	3x(Al <sub>0.44</sub> Ga <sub>0.56</sub> N/Al <sub>0.69</sub> Ga <sub>0.31</sub> N)	3x(2.0/3.6nm)
Current-spreading	Al <sub>0.77</sub> Ga <sub>0.23</sub> N:Si	1.2µm
Graded buffer	UID-AI <sub>1-0.77</sub> Ga <sub>0-0.23</sub> N	100nm
Buffer	AIN	600nm
Template	FFA Sp-AlN	

Figure 2. MOVPE layer sequence.

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The hillock density was affected by the sputter temperature:  $1.4 \times 10^5$ /cm<sup>2</sup> and  $4.9 \times 10^3$ /cm<sup>2</sup> on AlN sputtered at 700°C and 750°C, respectively. Clearly, the higher sputtering temperature is preferred. Previous FFA Sp-AlN grown at 600°C resulted in an even higher hillock density of ~ $10^6$ /cm<sup>2</sup>.

The researchers comment: "The occurrence of lower screw- and mixed-type FFA Sp-AIN dislocation densities at higher deposition temperatures can be attributed to the improvement in the c-axis orientational ordering of the AIN film in the as-sputtered state."

Working toward LED structures, the researchers then grew 1µm silicon-doped (:Si)  $AI_{0.75}Ga_{0.25}N$ on FFA Sp-AIN by MOVPE. The researchers studied the effects of MOVPE growth temperature, rate, and ammonia/hydrogen (NH<sub>3</sub>/H<sub>2</sub>) partial pressures on the AIGaN:Si surface morphology. To maintain the AI content of the layer at 75%, the researchers also varied the trimethyl-AI/Ga precursor supply to compensate.

The study showed that the interest of low hillock density was achieved through high temperature (1100°C) and low growth rate ( $0.5\mu$ m/h), along with high NH<sub>3</sub> partial pressure (10kPa). The effect of H<sub>2</sub> partial pressure was insignificant. The optimized conditions reduce the difference in growth rates between the regions near and far from the screw dislocations.

The researchers comment: "Notably, the hillock encap density was independent of the MOVPE growth conditions for AlGaN, as it was determined by the density of the screw- and mixed-type dislocations in the FFA Sp-AlN template."

The condition for reducing hillocks had the unfortunate effect of meandering step growth and pit formation. This was tackled with a 1050°C low-temperature 100nm AlGaN:Si interlayer before growing the UV-C LED structure.

The template for the UV-C material was FFA Sp-AlN sputtered at 750°C and annealed for 51 hours at 1600–1680°C. The MOVPE layer sequence (Figure 2) included a multiple quantum well (MQW) consisting of three well/barrier pairs grown at 1050°C. The MQW was given a low silicon doping concentration of  $3-5\times10^{17}$ /cm<sup>3</sup>, designed to reduce non-radiative recombination center density.

The 1.2µm n-type, silicon-doped current-spreading layer was mostly grown at 1150°C, except for the last 100nm grown at 1050°C.

X-ray analysis of the material suggested a hillock density of 7.2x10<sup>3</sup>/cm<sup>2</sup>. The researchers comment: "These values are the state-of-the-art low TDDs among the Al(Ga)N grown on sapphire substrates."

LEDs were fabricated with nickel/gold p-electrodes and titanium/aluminium/nickel/gold n-electrodes. The sap-



Figure 3. (a) Optical microscope image of as-mounted UV-C LED
bare die in ceramic package. Dashed rectangle: edge of die.
Photographs of packaged UV-C LEDs: (b) without and (c) with
encapsulation. (d) Electroluminescence spectra. (e)
Current-voltage-light output power, and (f) EQE results.

phire substrate was thinned and polished to  $150\mu m$ . The  $400\mu m \times 300\mu m$  dies were singulated through laser scribing.

The chips were packaged flip-chip mounted on a 6.8mmx6.8mm sized AIN-based container-type ceramic package with gold-tin eutectic bonding and AI reflector (Figure 3). Some of the devices were encapsulated in UV-transparent silicone resin composed of poly(dimethylsiloxane) and poly(diethoxy-siloxane).

The emitted UV-C radiation's peak wavelength was 263nm. Without encapsulation the external quantum efficiency reached 4.9% at 2.0mA injection current. Encapsulation increased the peak EQE to 8.0% at 2.9mA. The corresponding output powers at 20mA were 4.0 and 6.6mW, respectively. The researchers estimate the light extraction efficiency enhancement from encapsulation at 1.6x.

The high EQEs are attributed to improved internal quantum efficiency and, possibly, carrier injection efficiency. "The uniform thickness and AIN-molar fraction of the MQWs, AIN-EBL, and p-type AIGaN:Mg are attributable to the suppressed undesirable current crowding and overflow," the team writes. https://doi.org/10.35848/1882-0786/ac66c2 Author: Mike Cooke